1/3

+

100

110-

Accessing interrupt vector table information stored in a non-volatile memory.

120-

Initializing a program interrupt controller (PIC).

130-

Programming a system management (SM) bus controller.

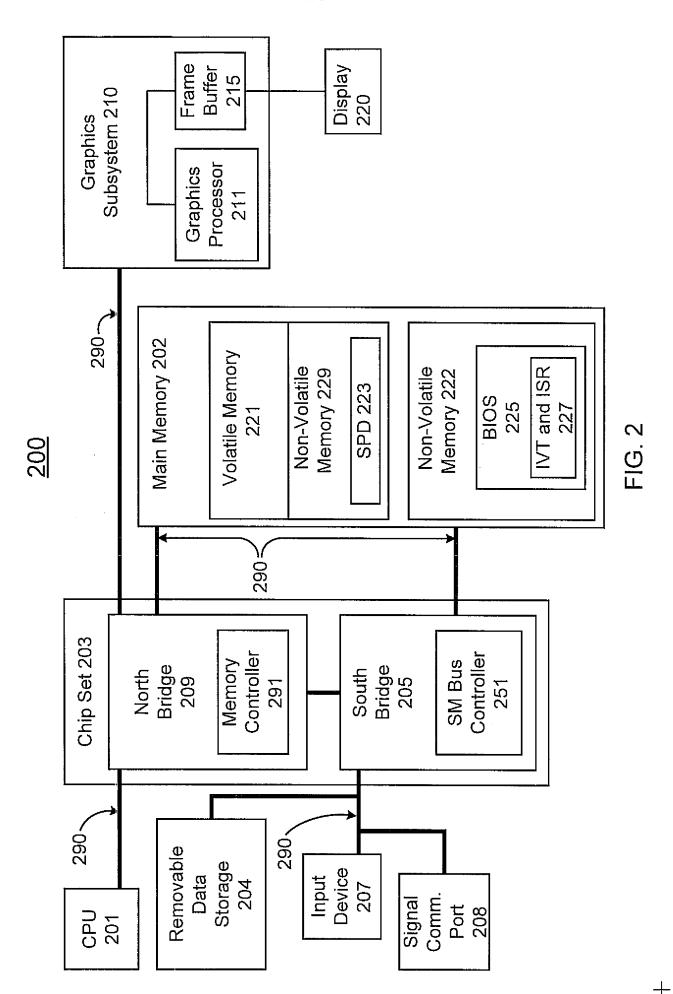
140-

Operating the SM bus controller in a multitasking environment in which said SM bus controller operates in an interrupt driven mode prior to completing volatile memory initialization.

(REPLACEMENT SHEET)

FIG. 1

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<u>300</u>

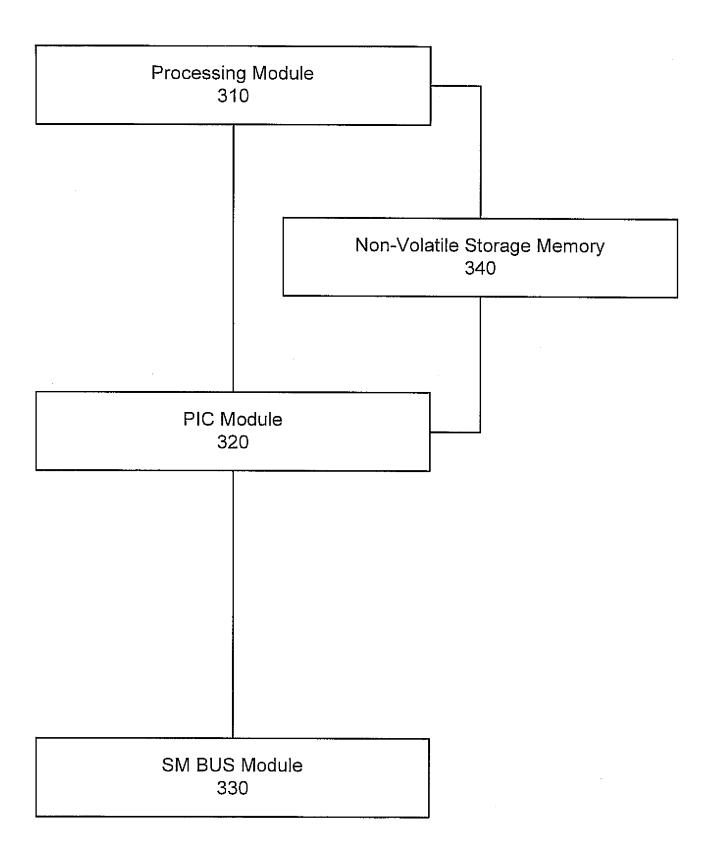


FIG. 3